Emerson VersaMax Connector

Devices compatible with SNP protocol.

- Name: SNP
- Version: 1.0.0.0
- Protocol: Proprietary
- Interface: Standard Serial Port
- Runtime: .Net 4.8 (Windows)
- Configuration:
  - Devices / Protocols

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Overview

Communication with devices compatible with SNP protocol.

Communication Driver Information	
Driver name	Snp
Assembly Name	T.ProtocolDriver.Snp
Assembly Version	1.0
Available for Linux	False
PLC types supported	GE Fanuc PLCs - 90/30 and 90/70 Series, PACSystem and VersaMax.
Interface	Serial and MultiSerial

SNP (Series Ninety Protocol) was created by GE Fanuc Automation (GE + Fanuc). Later the brand became GE Intelligent Platforms, and today the PLC line (PACSystems/VersaMax) is with Emerson.

Supported Operands

AI	Analog Input
AQ	Analog Output
G	Discrete Global Memory
I	Discrete Input
Q	Discrete Output
T	Output (Temporary) Coils
M	Internal Coils
S	System Status
R	Register Memory

Channels Configuration

Protocol Options

- **BlockSize:** Defines the maximum amount of items per group, the default value is **1000**.
- **PLC Type:** 90/30 or 90/70.
- **Level 2 password:** PLC write access authentication password.

Settings

- **Serial and MultiSerial channels:**

  Default configuration:

  DataBits: 8.

  StopBits: 1 if parity is used, 2 if no parity.
  Set the other fields according to your Serial or MultiSerial port configuration.

Nodes Configuration

Station Configuration

SlaveId: Set this field with the address of the slave device in the SNP network. They can be addressed from 1 to 247 for serial nodes, or 0 to 255 for TCP/IP nodes. The address 0 is used for the broadcast.

- **Serial channels:**

Station syntax: ***<SlaveId>***
Ex: 1

- **MultiSerial channels:**

Station syntax: ***<ComPort>;<SlaveId>***

Where : <ComPort> = COM Port of the slave device in the SNP network (default is com1).
Ex: com1;1

Points Configuration

The syntax for the SNP communication points are:

- ***%<Operand><Address>***

Operand: Indicates the memory area, the valid values are:

AI	Analog Input
AQ	Analog Output
G	Discrete Global Memory
I	Discrete Input
Q	Discrete Output
T	Output (Temporary) Coils
M	Internal Coils
S	System Status
R	Register Memory

Address: Indicates the data address in the memory area.

Ex: %R00001 (Operand = R, Address = 1)

Driver Revision History

SNP Driver Revision History	
Version	**Notes**
1.0.0.0	Initial release on new documentation standards

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